



**Address:**  
**JAMBU PERSHAD & SONS**  
6275/22 Nicholson Road,  
Ambala Cantt, Haryana,  
INDIA  
Pin: 133001

**Email:**  
sales@japson.com  
japsonambala@yahoo.com

**Website:**  
www.japson.com  
**Phone:**  
+91-171-4006897

# Verification of Truth Tables of Logic Gates Experiment Apparatus

## Product Image



## Description

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Verification of truth tables of Logic gates using Universal gates (NAND & NOR gates)

Objective : To verify the truth table of AND gate, OR gate, NOR gate, NAND gate & NOT gate using NAND & NOR gate.

Features : Instrument comprises of DC Regulated Power Supply 5V/150mA, 2 logic 1 & 2 logic 0 inputs are provided on sockets, 2 Red LED output indicators, circuit diagram printed for 4 NAND & 4 NOR gates & their respective ICs placed inside the cabinet & connections brought out at sockets.

<b>Catalog No.</b>	<b>Particulars</b>
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JE21210	Verification of Truth Tables of Logic Gates Experiment Apparatus
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## Disclaimer

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The Products details given on this page are indicative in nature and JAPSON reserves the right to change them without prior notice. Buyer is also requested to re-check the specifications and other features of product at the time of order as product development is a continuous process and minor modifications may be made to design based on latest availability, process and design.