



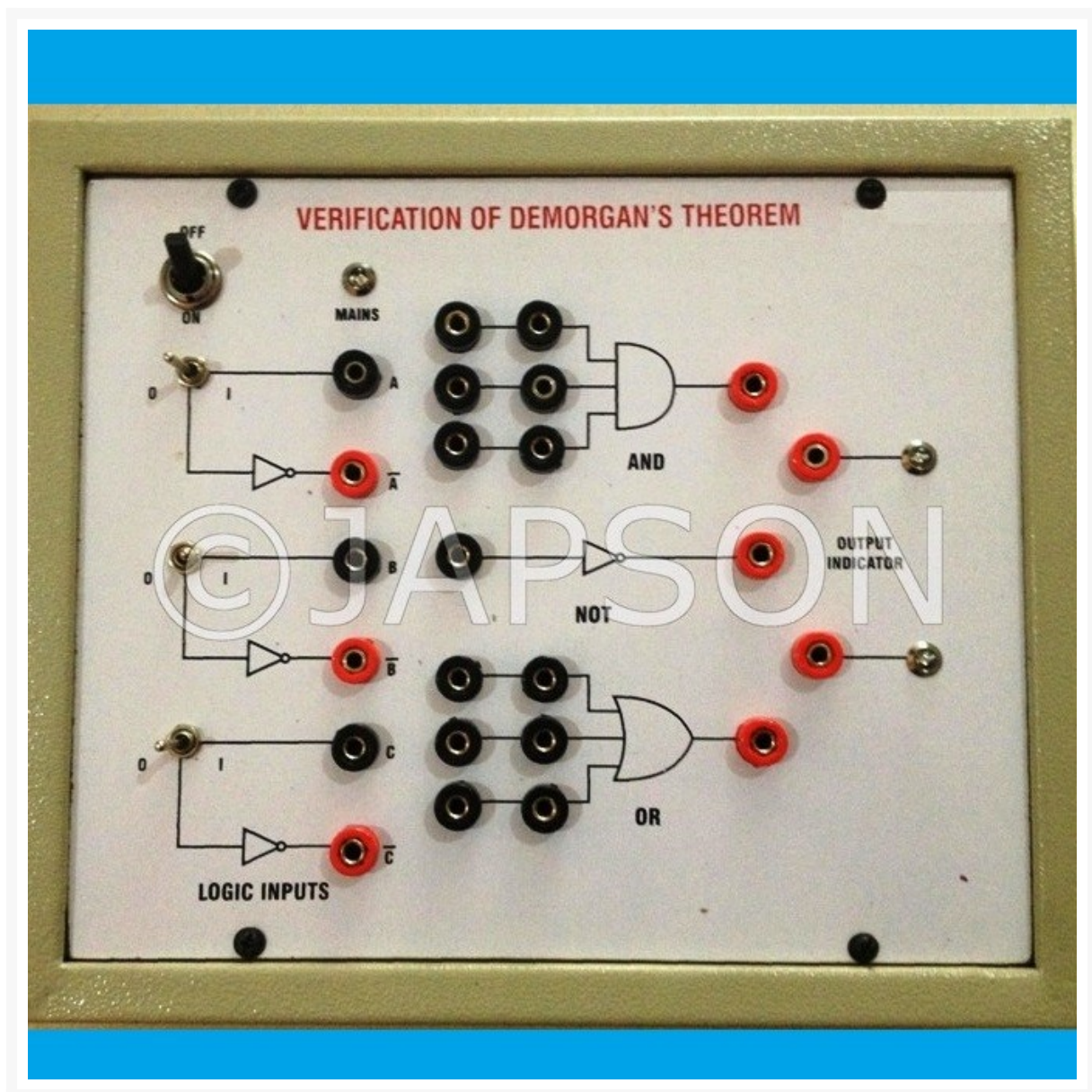
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# Verification of De Morgan Theorems Experiment Apparatus

## Product Image



## Description

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**Objective :** To verify the truth table of Demorgans Theorems.

**Features :** Instrument comprises of DC Regulated Power Supply 5V/150mA, ICs placed inside the cabinet & connections brought out at sockets, 4 SDPT switches provided for selecting logic 1 & logic 0 , 2 Red LED output indicators.

Catalog No.	Particulars
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JE21300	Verification of DeMorgan Theorems Experiment Apparatus Experiment Apparatus
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## Disclaimer

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The Products details given on this page are indicative in nature and JAPSON reserves the right to change them without prior notice. Buyer is also requested to re-check the specifications and other features of product at the time of order as product development is a continuous process and minor modifications may be made to design based on latest availability, process and design.