



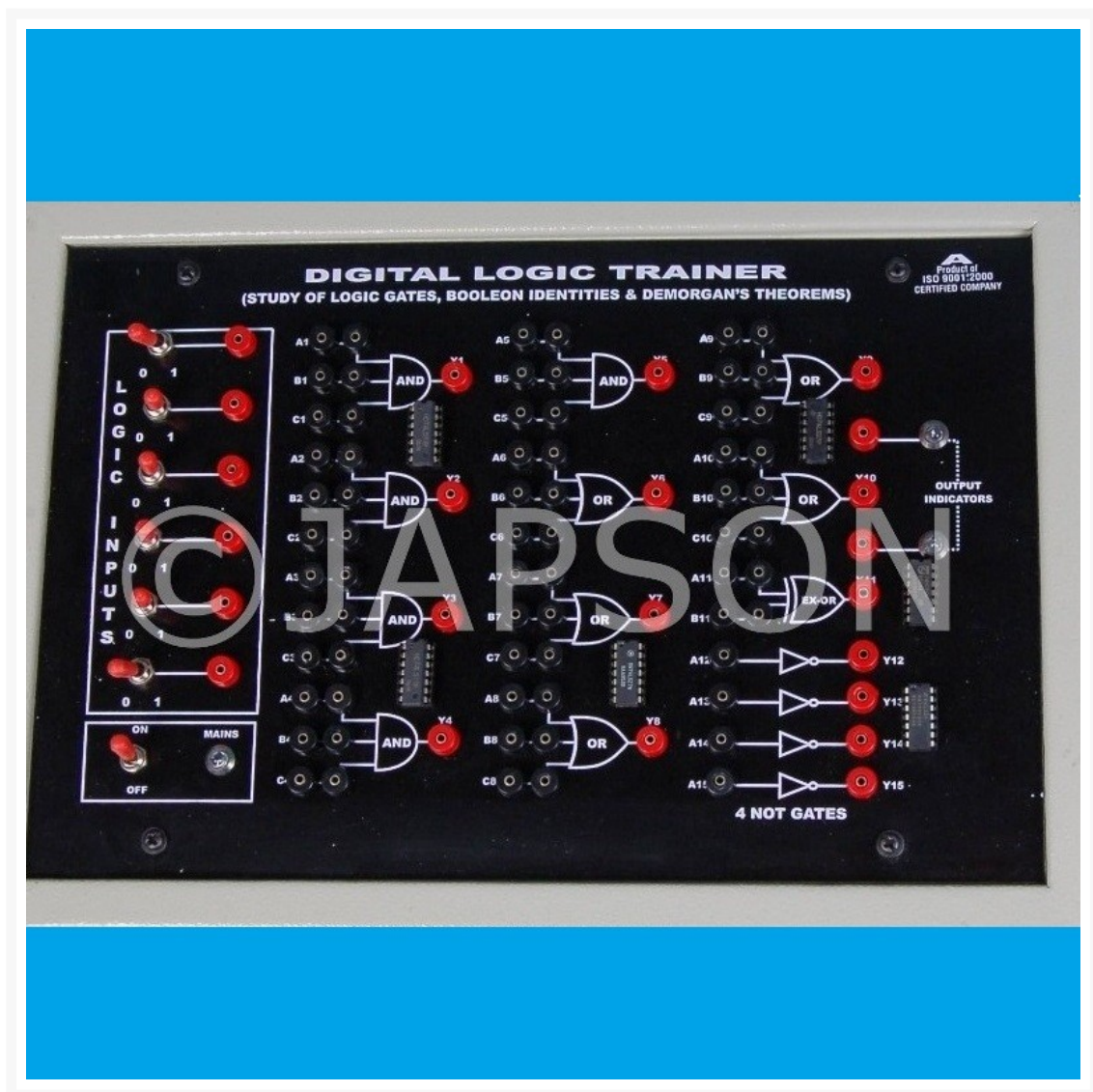
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Verification of Boolean Identity & Demorgans Theorem Experiment Apparatus

Product Image



Description

Objective : To verify the truth table of 3 input AND gate, OR gate, NAND gate, NOR gate, single input NOT gates, Boolean expressions & Demorgans Theorems.

Features : Instrument comprises of DC Power supply 5V/150mA, 4 SPDT switches provided for selecting logic 1 & logic 0, 2 Red LED output indicators, circuit diagram printed for 5 OR, 5 AND & 4 NOT gates & their respective ICs placed inside the cabinet & connections brought out at sockets.

Catalog No.	Particulars
JE21290	Verification of Boolean Identity & Demorgans Theorem Experiment Apparatus

Disclaimer

The Products details given on this page are indicative in nature and JAPSON reserves the right to change them without prior notice. Buyer is also requested to re-check the specifications and other features of product at the time of order as product development is a continuous process and minor modifications may be made to design based on latest availability, process and design.