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Time Division Multiplexer (TDM) Experiment Apparatus

Product Image



Description

It has facility to study Time Division Multiplexing and PAM signal from four different input channels and to learn De-multiplexing and Low-Pass filtering process. Input channels : 4 numbers Switching : Time Division Multiplexing Modulation : Pulse Amplitude Modulation Sampling rate : 16 KHz Maximum signal BW : 2 KHz Onboard sine waves : 2KHz, 1KHz, 500Hz,250Hz, 0-5V variable amplitude Synchronization pulse generation: Using variable DC level Phase lock loop : Generates receiver clock and channel information Low pass filter type : 4th order Butterworth filter Interconnection : 2mm banana socket Power Supply : +5V, +/-12V

Catalog No. Particulars

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Disclaimer

The Products details given on this page are indicative in nature and JAPSON reserves the right to change them without prior notice. Buyer is also requested to re-check the specifications and other features of product at the time of order as product development is a continuous process and minor modifications may be made to design based on latest availability, process and design.