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# Study of Logic Gates using TTL ICs (7 in 1) Experiment Apparatus

## Product Image

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## Description

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**Objective :** To verify the truth table for TTL AND, OR, NOT, NAND, NOR, EX-OR & EX-NOR gates.

**Features :** Instrument comprises of 5V/150mA DC Regulated Power Supply for logic 1 & logic 0.

### Catalog No.    Particulars

JE21270            Study of Logic Gates using TTL ICs (7 in 1) Experiment Apparatus

## Disclaimer

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The Products details given on this page are indicative in nature and JAPSON reserves the right to change them without prior notice. Buyer is also requested to re-check the specifications and other features of product at the time of order as product development is a continuous process and minor modifications may be made to design based on latest availability, process and design.