



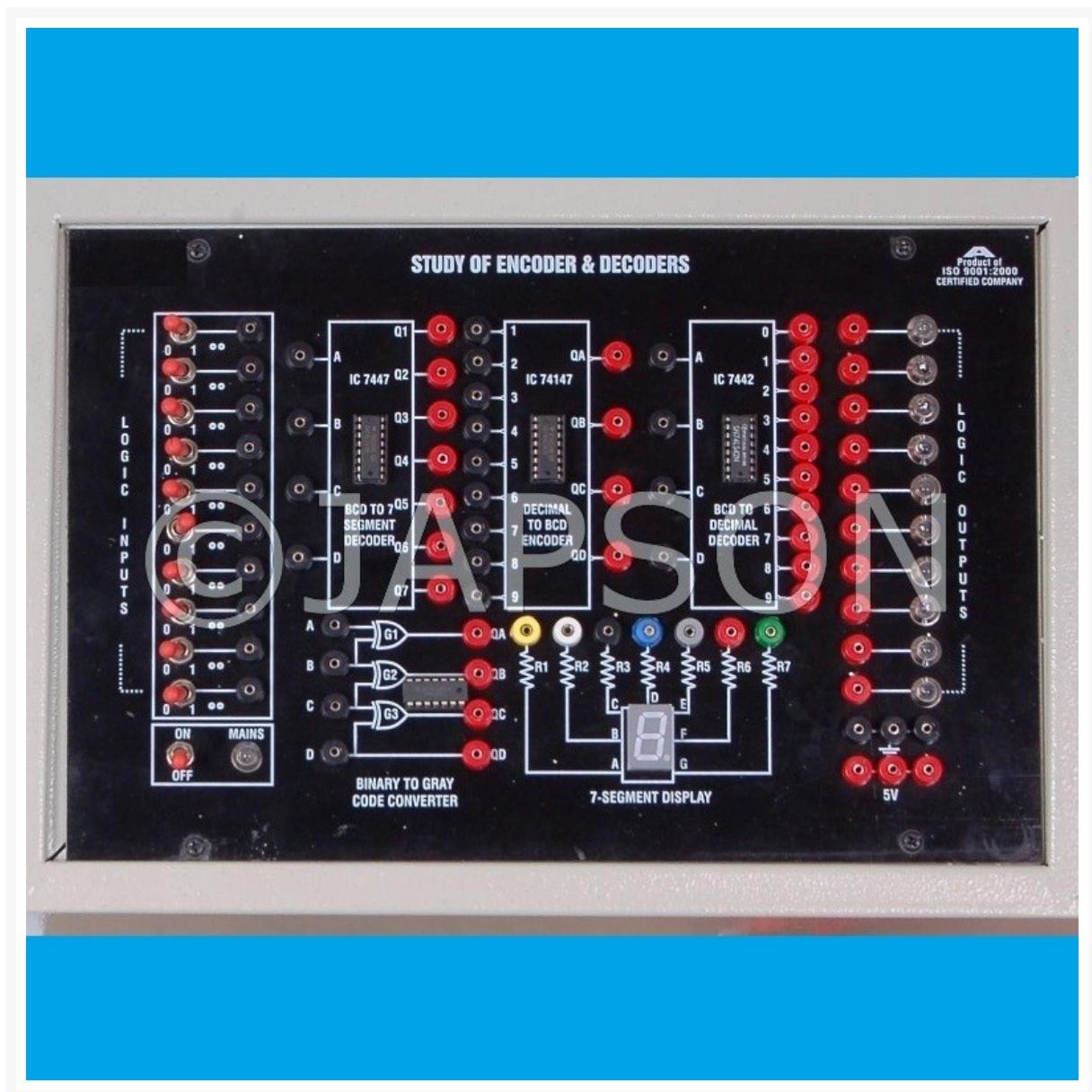
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Study of Encoder & Decoder Circuits Experiment Apparatus

Product Image



Description

Objective : To study and verify the truth tables of binary to gray code convertor, bcd to seven segment decoder, bcd to decimal decoder, decimal to bcd encoder.

Features : Instrument comprises of dc regulated power supply 5vdc/150mA, 4 spdt switches provided for selecting logic 1 & logic 0, 1hz monoshot clock pulse, 4 output indicators, circuit diagram for ic 7490, ic7447 & 7-segment display printed & connections for various inputs & outputs brought out at the sockets on the front panel.

Catalog No. Particulars

JE21230 Study of Encoder & Decoder Circuits Experiment Apparatus

Disclaimer

The Products details given on this page are indicative in nature and JAPSON reserves the right to change them without prior notice. Buyer is also requested to re-check the specifications and other features of product at the time of order as product development is a continuous process and minor modifications may be made to design based on latest availability, process and design.