



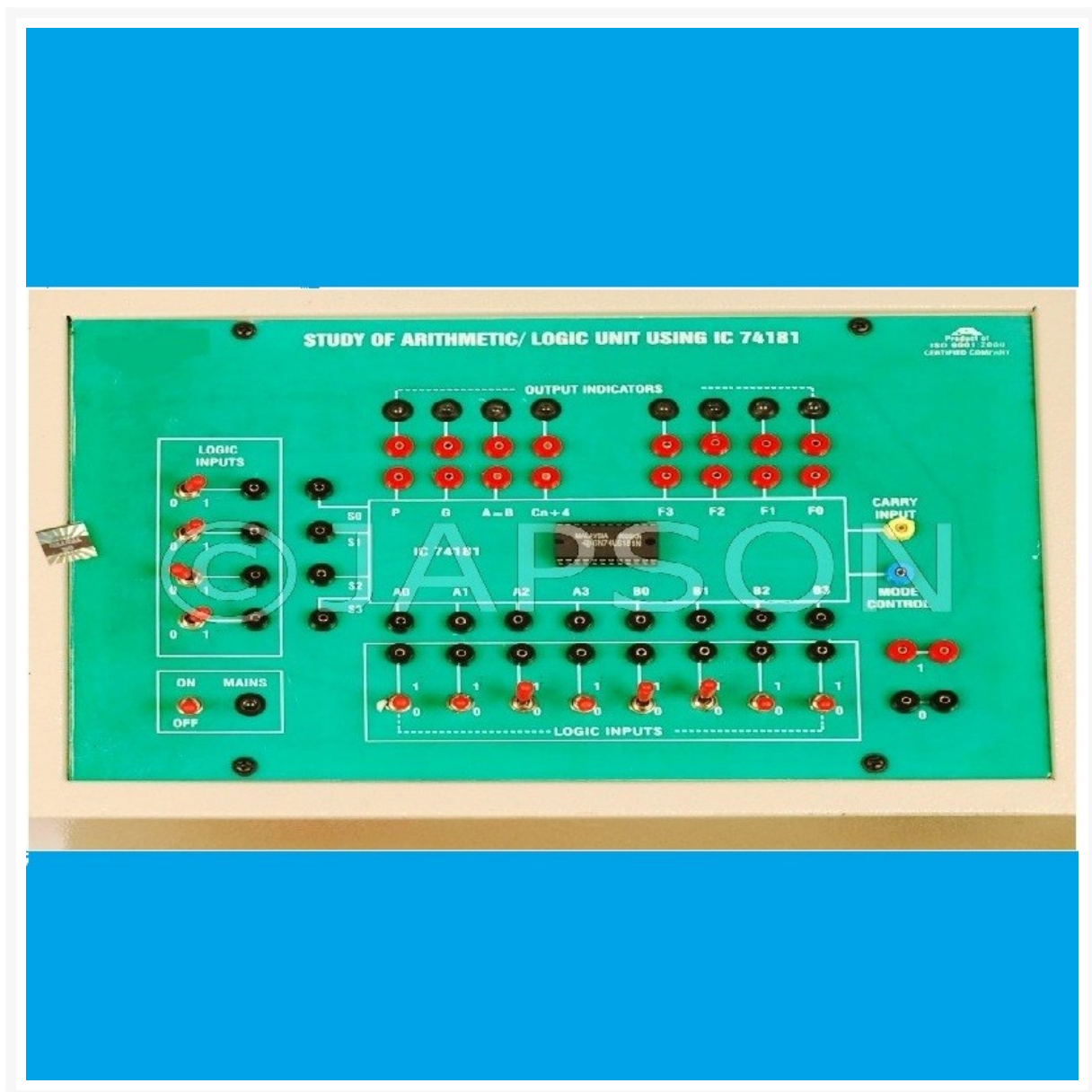
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Study of Arithmetic Logic Unit (ALU) Experiment Apparatus

Product Image



Description

Features : Instruments Comprises of fixed Output DC Regulated power supply of 5V. Logic inputs selectable using SPDT switches & logic output indicators & IC 74181. Circuit diagram is printed on the front panel.

Catalog No.	Particulars
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JE21250	Study of Arithmetic Logic Unit (ALU) Experiment Apparatus
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Disclaimer

The Products details given on this page are indicative in nature and JAPSON reserves the right to change them without prior notice. Buyer is also requested to re-check the specifications and other features of product at the time of order as product development is a continuous process and minor modifications may be made to design based on latest availability, process and design.