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# Study of 4 Bit Adder and Subtractor Experiment Apparatus

## Product Image



## Description

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**Objective :** To study the 4 bit adder and subtractor using ic 7483

**Features :** Instrument comprises of dc regulated power supply 5v/150ma, 4 logic inputs selectable using spdt switches, 4 output led indicators, circuit diagram printed & connections brought out at sockets on the front panel.

Catalog No.	Particulars
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JE21220	Study of 4 Bit Adder and Subtractor Experiment Apparatus
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## Disclaimer

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The Products details given on this page are indicative in nature and JAPSON reserves the right to change them without prior notice. Buyer is also requested to re-check the specifications and other features of product at the time of order as product development is a continuous process and minor modifications may be made to design based on latest availability, process and design.