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Study & Verify Truth Tables of JK Master Slave Flip Flop Experiment Apparatus

Product Image



Description

Objectives : To verify truth tables of Jk & JK Master slave flip flops using IC 7472 & IC 7476. Features : Instrument comprises of DC Regulated Power Supply 5VDC/150mA, 4 SPDT switches provided for selecting logic 1 & logic 0, 1Hz monoshot clock pulse, two output indicators, Circuit diagram for JK & JK Master slave flip-flops are printed & connections for input & outputs brought out at the sockets on the front panel.

Catalog No. Particulars

Disclaimer

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