



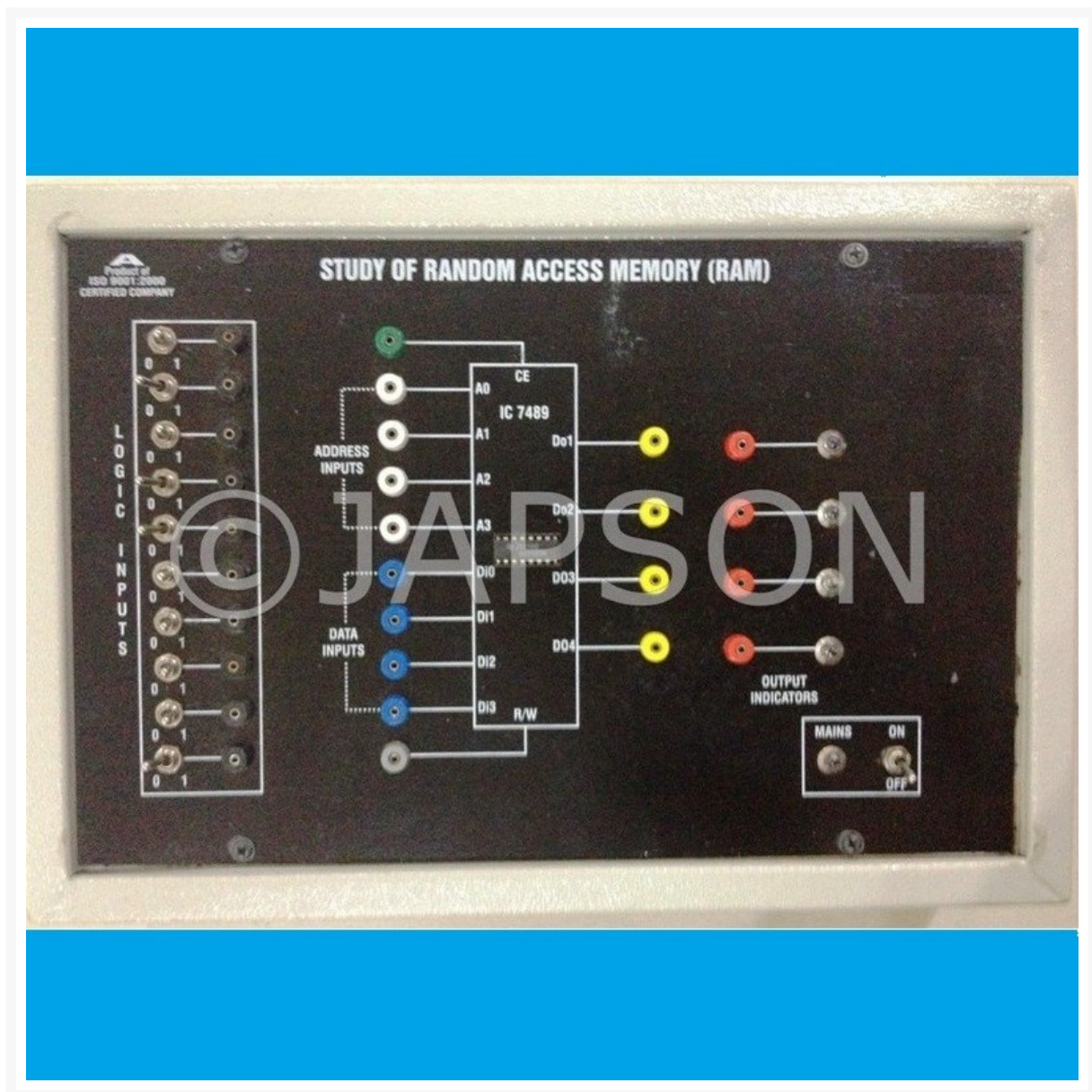
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RAM Circuit using IC 7489 Experiment Apparatus

Product Image



Description

Objective : To Study and verify the truth table of RAM circuit.

Features : Instrument comprises of DC Regulated Power Supply 5VDC/150mA, ten logic inputs selectable using SPDT switches, four LED output indicators, IC 7489 placed on the front panel & connections brought out at sockets.

Catalog No.	Particulars
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JE21370	RAM Circuit using IC 7489 Experiment Apparatus
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Disclaimer

The Products details given on this page are indicative in nature and JAPSON reserves the right to change them without prior notice. Buyer is also requested to re-check the specifications and other features of product at the time of order as product development is a continuous process and minor modifications may be made to design based on latest availability, process and design.