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# Pulse Code Modulation & Demodulation Experiment Apparatus

## Product Image

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## Description

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It has facility to generate the binary code of input signal, using analog to digital converter and time division multiplexing of two such PCM data stream.

Input channels : 2 Nos. Time-division multiplexed, Pulse code modulated

Onboard signals : 1KHz, 500 Hz variable amplitude synchronized sine-waves. Two variable amplitude DC levels

Synchronization signal : Pseudo random code generation

Parity code facility : Even, Odd, Hamming, No Parity

Switch Fault mode : Facility to create signal bit error using DIP switch

LED Display : At every functional block for examination of Digital data and control signal

Mode of Operation : Fast/Slow

Interconnection : 2mm banana socket

Power Supply : +5V, +/-12V

<b>Catalog No.</b>	<b>Particulars</b>
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JE21870	Pulse Code Modulation & Demodulation Experiment Apparatus
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## Disclaimer

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The Products details given on this page are indicative in nature and JAPSON reserves the right to change them without prior notice. Buyer is also requested to re-check the specifications and other features of product at the time of order as product development is a continuous process and minor modifications may be made to design based on latest availability, process and design.