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# Flip Flops using TTL ICs and NAND Gates Experiment Apparatus

## Product Image



## Description

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**Objective :** To study & verify Truth tables of RS, D Type, Flip Flops using NAND Gates & D, T & JK flip flops using TTL ICs.

**Features :** Instrument comprises of DC Regulated Power Supply 5VDC/150mA, 4 logic inputs, logic 1 & logic 0 selectable using SPDT switches, 1Hz monoshot clock pulse, two output indicators, Circuit diagram for RS , D , T & JK & JK Master slave flipflops are printed, their respective ICs placed inside the cabinet & connections brought out on the sockets. NOT gate for converting RS flip-flops to D type flipflop.

### Catalog No.    Particulars

JE21320            Flip Flops using TTL ICs and NAND Gates Experiment Apparatus

## Disclaimer

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The Products details given on this page are indicative in nature and JAPSON reserves the right to change them without prior notice. Buyer is also requested to re-check the specifications and other features of product at the time of order as product development is a continuous process and minor modifications may be made to design based on latest availability, process and design.