



Address:
JAMBU PERSHAD & SONS
6275/22 Nicholson Road,
Ambala Cantt, Haryana,
INDIA
Pin: 133001

Email:
sales@japson.com
japsonambala@yahoo.com

Website:
www.japson.com
Phone:
+91-171-4006897

Digital Electronic Circuit Trainer (Bread Board Model) Experiment Apparatus

Product Image



Description

Objective : A general purpose experimental board to construct more than 60 experiments of Digital Electronics e.g. Construction & Verification of truth tables of Logic Gates, Boolean Identities and DeMorgan Theorems, Digital Adder & Subtractors, Flip Flops, Counters, Shift Registers, Encoder & Decoders, Multiplexer & De multiplexer etc.

Specifications : It comprises of the Bread Board having:-two Nos Vertically Common Strip (Total 1280 Tie Points) and four Nos. Horizontally Common Strips (Total 400 Tie Points), Fixed output DC regulated power supply of 5V/1A and $\pm 15V/500mA$, 10 logic input switches logic 0 and logic 1, Ten LED output indicators & one 7- segment displays are provided, 1 Hz

moonshot clock pulse with pulser switch & Clock Generator with 1Hz,1KHz, & 10KHz selectable using band switch, One DPM (Digital Panel Meter) is mounted on the front panel.

Catalog No.	Particulars
JE21550	Digital Electronic Circuit Trainer (Bread Board Model) Experiment Apparatus

Disclaimer

The Products details given on this page are indicative in nature and JAPSON reserves the right to change them without prior notice. Buyer is also requested to re-check the specifications and other features of product at the time of order as product development is a continuous process and minor modifications may be made to design based on latest availability, process and design.