



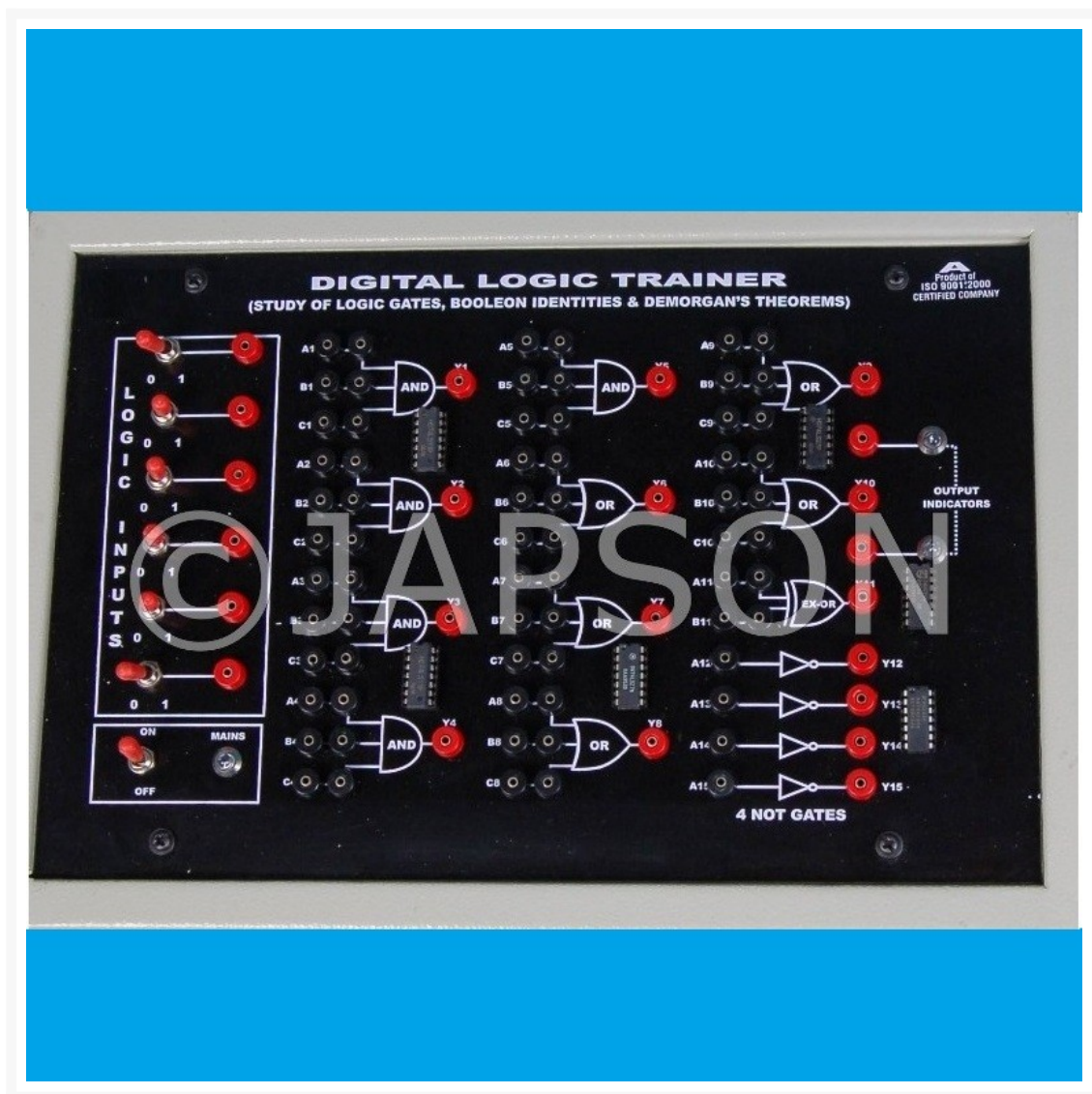
**Address:**  
**JAMBU PERSHAD & SONS**  
6275/22 Nicholson Road,  
Ambala Cantt, Haryana,  
INDIA  
Pin: 133001

**Email:**  
sales@japson.com  
japsonambala@yahoo.com

**Website:**  
www.japson.com  
**Phone:**  
+91-171-4006897

# Basic Logic Gates using Discrete Components (5 in 1) Experiment Apparatus

## Product Image



## Description

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**Objective :** To verify the truth table for DTL AND, OR, NOT, NAND, & NOR gates.

**Features :** Instrument comprises of 5V/150mA DC Regulated Power Supply for logic 1 & logic 0. Circuit diagram for OR, AND & NOT gates has been printed & components mounted on front panel to demonstrate the working of DTL gates. NAND & NOR gates can be assembled using different combinations of Gates.

Catalog No.	Particulars
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JE21280	Basic Logic Gates using Discrete Components (5 in 1) Experiment Apparatus
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## Disclaimer

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The Products details given on this page are indicative in nature and JAPSON reserves the right to change them without prior notice. Buyer is also requested to re-check the specifications and other features of product at the time of order as product development is a continuous process and minor modifications may be made to design based on latest availability, process and design.